

Optimization of Etch, Release, and Transfer of GaN HEMTs Devices

Emanuela Riglioni¹, Owen Meilander¹, and Mona Ebrish²

¹ Interdisciplinary Materials Science, Vanderbilt University, Nashville TN, USA 37215

² Department of Electrical Engineering, Vanderbilt University, Nashville TN, USA 37215

1 BACKGROUND

GaN High Electron Mobility devices

- ✓ GaN's wide bandgap
- ✓ High electron mobility at the GaN/AlGaIn interface
- ✓ Reliance on intrinsic charge carriers
- ✗ Require heterogeneous integration due to absence of logic
- ✗ Challenging integration into heterogeneous systems at large scale

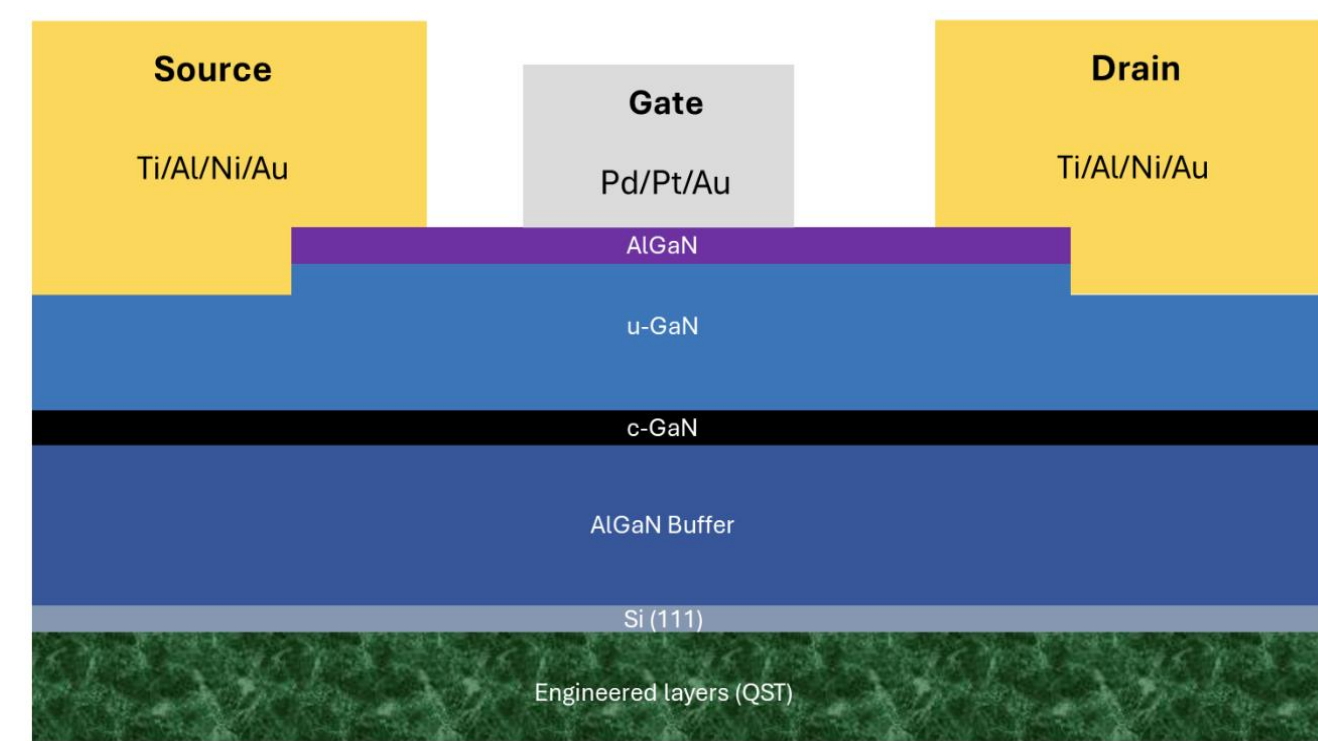


Figure 1: Cross section of a depletion mode device

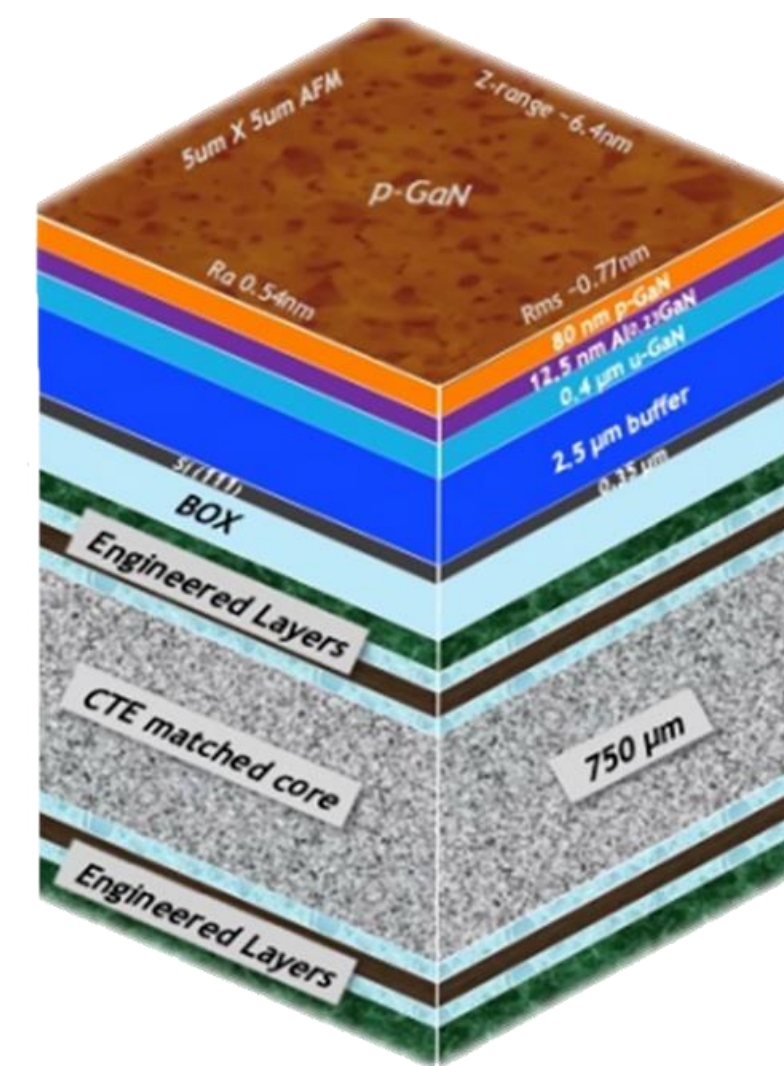
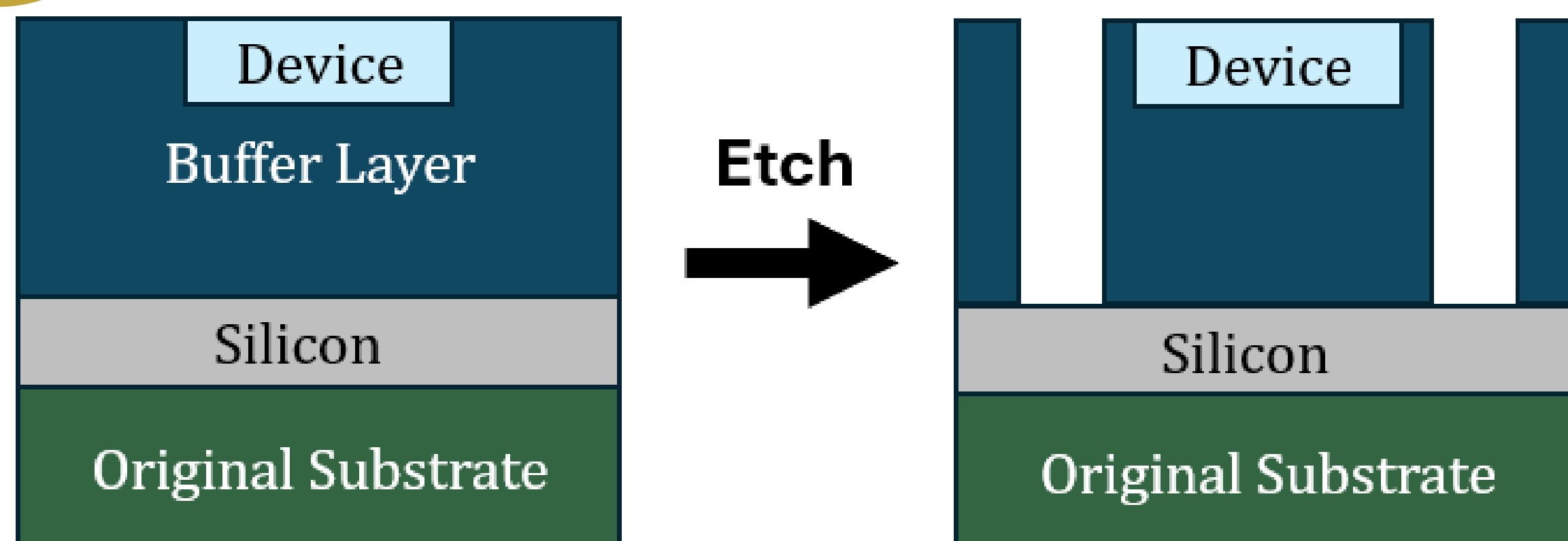


Figure 2: Qromis QST commercially available wafers

2 ETCH



- A nickel (Ni) hard mask was developed to provide a more robust etching process
- Explored two lithographic approaches, and both single and multicycles etching processes

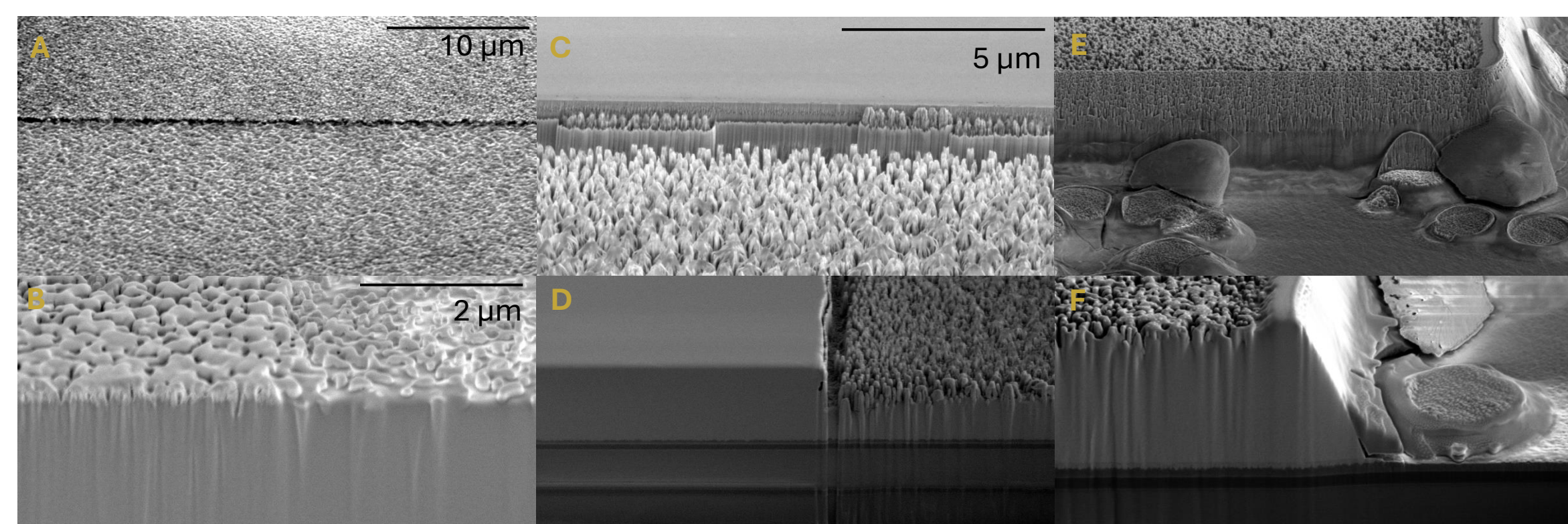
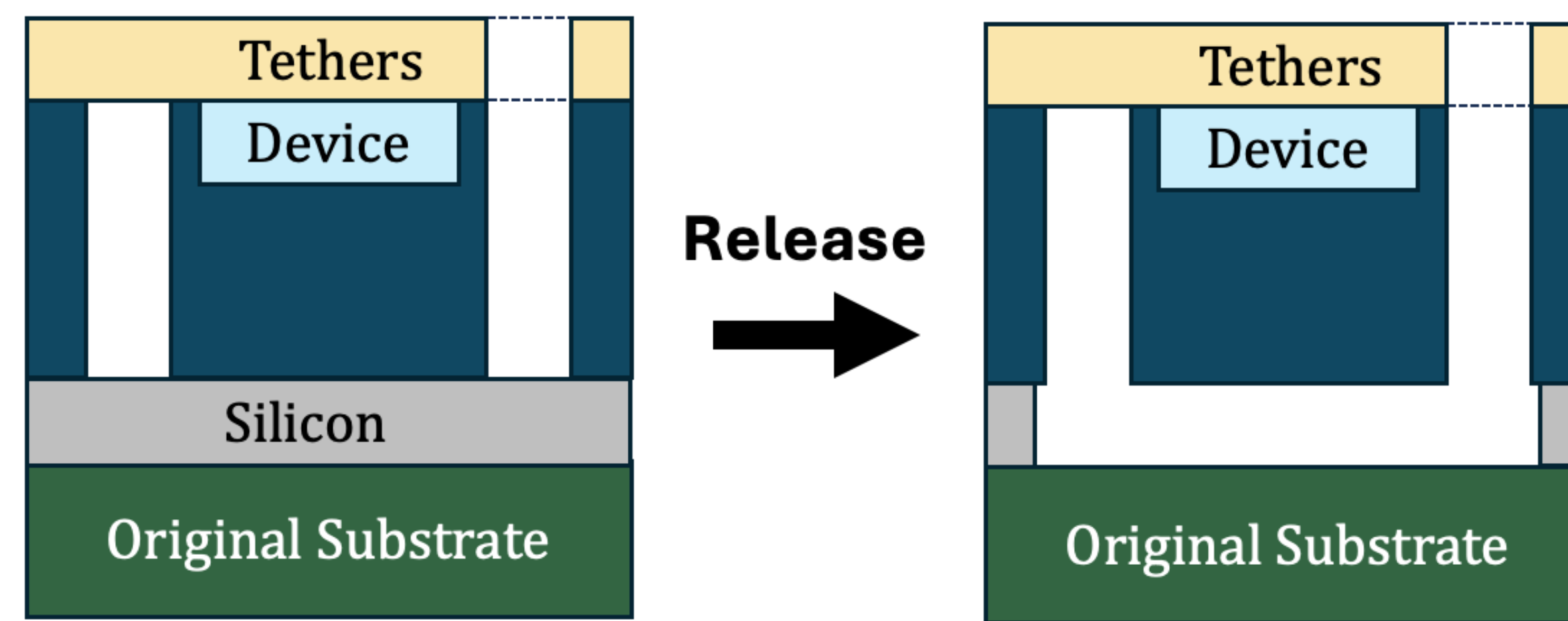


Figure 3. SEM cross sections of etched samples with hard mask developed using approach 2 (A,B) in single cycles, and approach 1 in single cycles (C,D) and multicycles (E,F)

3 RELEASE



- Device release was facilitated by tethers designed to stabilize GaN HEMTs during selective silicon layer etching using XeF₂
- Early tether designs faced stability issues ("popcorn effect"), prompting adjustments in size and configuration to improve reliability during release

4 TRANSFER

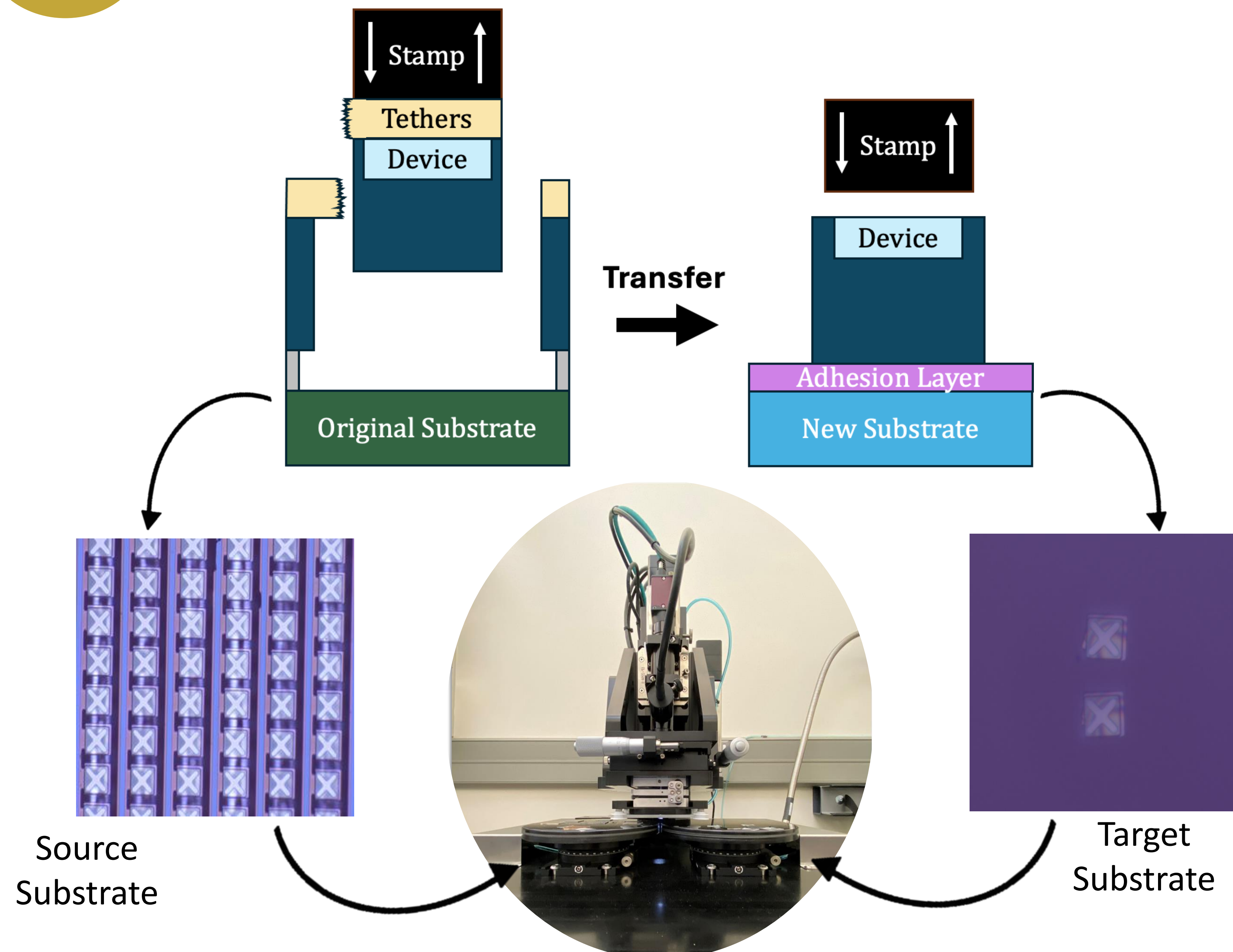


Figure 4. Illustration of micro-transfer printing process using a X-CelePrint micro-transfer printer, showing device on source substrate and transferred onto the target substrate

- Micro-transfer printing technique was used to transfer released devices onto the adhesive substrate

5 ADHESION LAYER

- Thickness of the adhesion layer crucial for its role as insulator or conductor
- 67 nm (1:10) and 5 μm (1:0) samples tested for extreme thicknesses
- 1:10 samples survived harsh temperatures environment

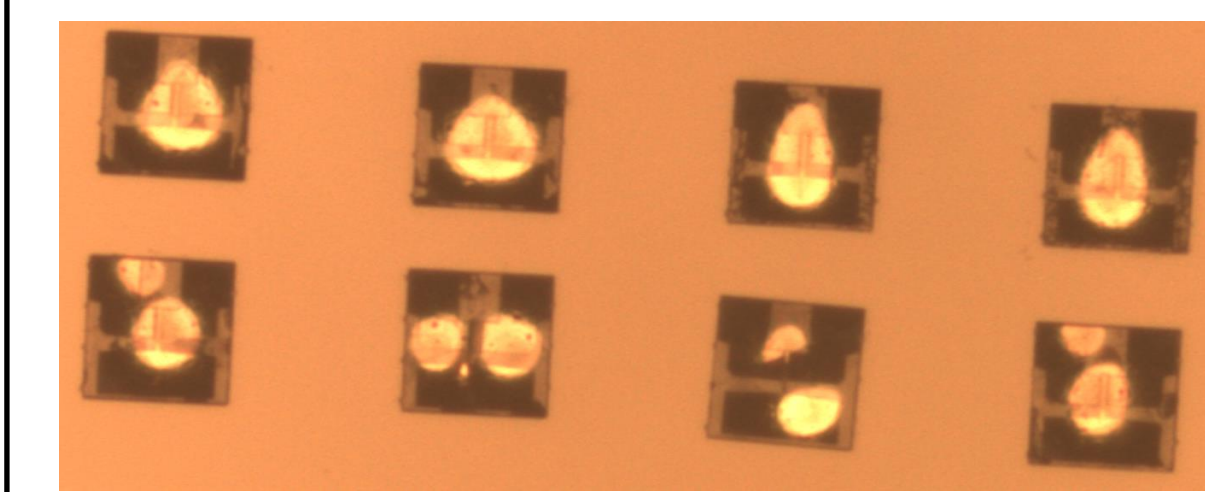
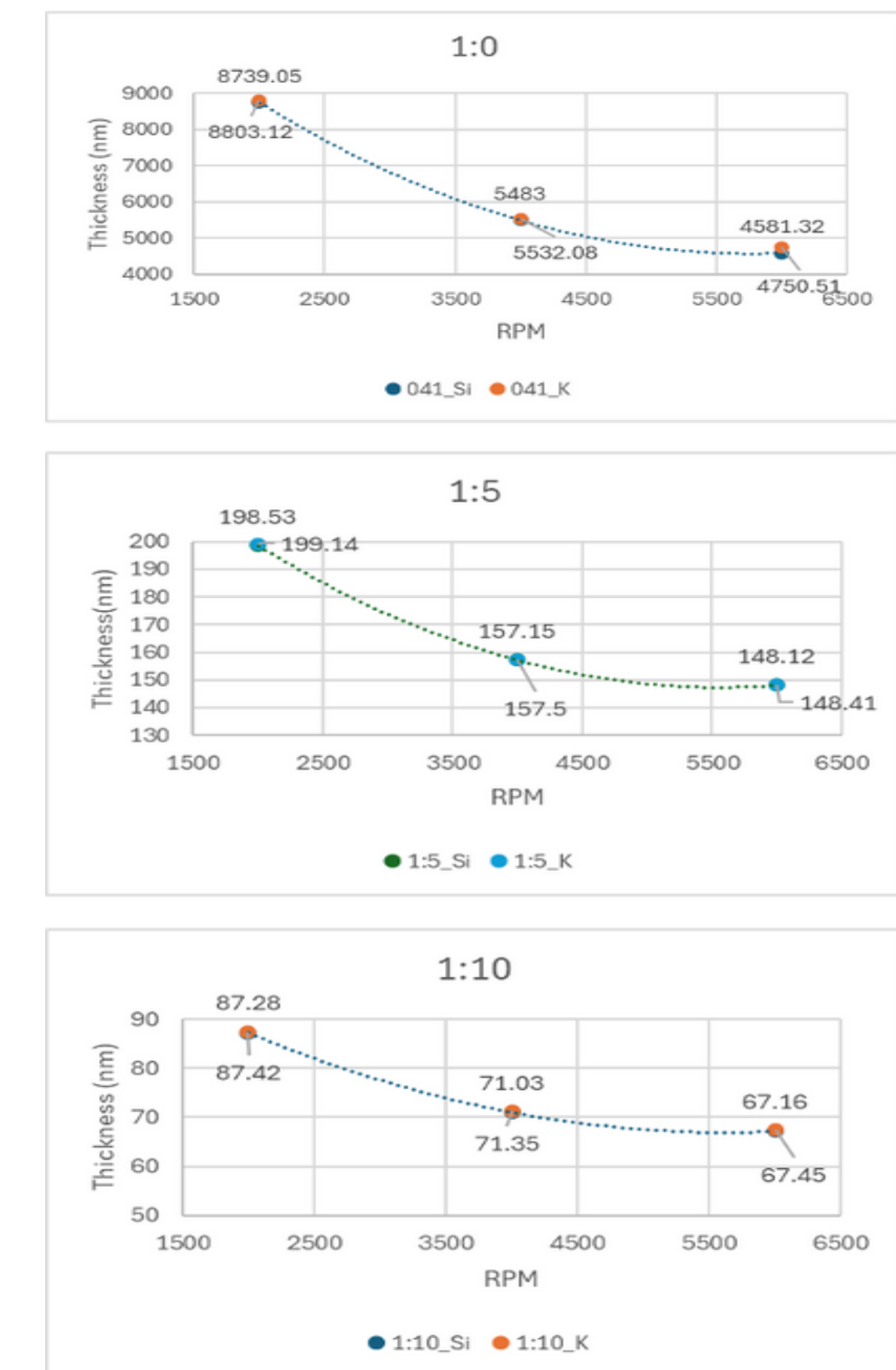


Figure 4. Transferred devices on 1:0 PR041 in Lake Shore vacuum chamber for thermal testing.

6 FUTURE WORK

- Optimize etching protocol to reach silicon layer
- Test improved tether designs for device release
- Investigate P041 performance at smaller thicknesses
- Refine transfer process for varied device release conditions
- Explore applications enabled by temperature-stable adhesion layer

7 ACKNOWLEDGMENTS

The authors acknowledge funding support from the Air Force Office of Scientific Research (AFRL grant FA9550-23-1-06209) and DARPA/Dol (Contract No. 13884643). VINSE is also gratefully acknowledged for providing facilities for imaging and fabrication. Finally, the authors thank KAYAKU Advanced Materials.